



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Marc Tremblay and William N. Joy

Title: IMPLICITLY DERIVED REGISTER SPECIFIERS IN A PROCESSOR

Application No.: 09/204,479

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**PRELIMINARY AMENDMENT**

Prior to the first action on the merits, please amend the above-identified application as follows:

In the Claims

Please amend claims as follows:

1. (Twice Amended) A processor comprising:

a register file; and

a functional unit, coupled to the register file, that executes an instruction that operates upon plural registers of said register file, including at least one register explicitly identified by an explicitly defined register specifier and at least one other register implicitly identified by the explicitly-defined register specifier.

3. (Amended) A processor according to Claim 1 wherein:

a register specifier for the other register is implicitly derived by adding one to the explicitly-defined register specifier.